

The RF Line NPN Silicon Power Transistor

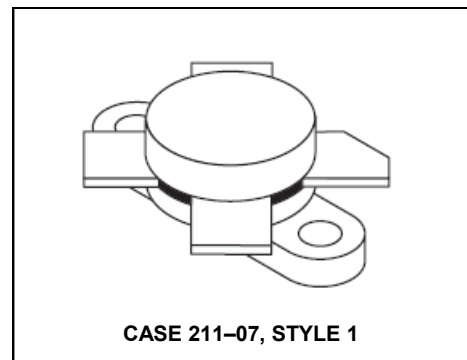
60W, 30MHz, 12.5V

Rev. V1

Designed for power amplifier applications in industrial, commercial and amateur radio equipment to 30 MHz.

- Specified 12.5 V, 30 MHz characteristics —
Output power = 60 W
Minimum gain = 13 dB
Efficiency = 55%

Product Image



MAXIMUM RATINGS

Rating	Symbol	Value	Unit
Collector–Emitter Voltage	V_{CEO}	18	Vdc
Collector–Emitter Voltage	V_{CES}	36	Vdc
Emitter–Base Voltage	V_{EBO}	4.0	Vdc
Collector Current — Continuous	I_C	15	Adc
Total Device Dissipation @ $T_C = 25^\circ\text{C}$ Derate above 25°C	P_D	175 1.0	Watts W/ $^\circ\text{C}$
Storage Temperature Range	T_{stg}	–65 to +150	$^\circ\text{C}$

THERMAL CHARACTERISTICS

Characteristic	Symbol	Max	Unit
Thermal Resistance, Junction to Case	$R_{\theta JC}$	1.0	$^\circ\text{C/W}$

ELECTRICAL CHARACTERISTICS ($T_C = 25^\circ\text{C}$ unless otherwise noted.)

Characteristic	Symbol	Min	Typ	Max	Unit
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OFF CHARACTERISTICS

Collector–Emitter Breakdown Voltage ($I_C = 100\text{ mAdc}$, $I_B = 0$)	$V_{(BR)CEO}$	18	—	—	Vdc
Collector–Emitter Breakdown Voltage ($I_C = 50\text{ mAdc}$, $V_{BE} = 0$)	$V_{(BR)CES}$	36	—	—	Vdc
Emitter–Base Breakdown Voltage ($I_E = 10\text{ mAdc}$, $I_C = 0$)	$V_{(BR)EBO}$	4.0	—	—	Vdc

ON CHARACTERISTICS

DC Current Gain ($I_C = 5.0\text{ Adc}$, $V_{CE} = 5.0\text{ Vdc}$)	h_{FE}	10	—	150	—
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DYNAMIC CHARACTERISTICS

Output Capacitance ($V_{CB} = 12.5\text{ Vdc}$, $I_E = 0$, $f = 1.0\text{ MHz}$)	C_{ob}	—	—	250	pF
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(continued)

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ELECTRICAL CHARACTERISTICS — continued ($T_C = 25^\circ\text{C}$ unless otherwise noted.)

Characteristic	Symbol	Min	Typ	Max	Unit
FUNCTIONAL TESTS (Figure 1)					
Common-Emitter Amplifier Power Gain ($V_{CC} = 12.5\text{ Vdc}$, $P_{out} = 60\text{ W}$, $f = 30\text{ MHz}$)	G_{pe}	13	—	—	dB
Collector Efficiency ($V_{CC} = 12.5\text{ Vdc}$, $P_{out} = 60\text{ W}$, $f = 30\text{ MHz}$)	η	55	—	—	%
Series Equivalent Input Impedance ($V_{CC} = 12.5\text{ Vdc}$, $P_{out} = 60\text{ W}$, $f = 30\text{ MHz}$)	Z_{in}	—	$1.66 - j.844$	—	Ohms
Series Equivalent Output Impedance ($V_{CC} = 12.5\text{ Vdc}$, $P_{out} = 60\text{ W}$, $f = 30\text{ MHz}$)	Z_{out}	—	$1.73 - j.188$	—	Ohms
Parallel Equivalent Input Impedance ($V_{CC} = 12.5\text{ Vdc}$, $P_{out} = 60\text{ W}$, $f = 30\text{ MHz}$)	Z_{in}	—	$2.09/1030$	—	Ω/pF
Parallel Equivalent Output Impedance ($V_{CC} = 12.5\text{ Vdc}$, $P_{out} = 60\text{ W}$, $f = 30\text{ MHz}$)	Z_{out}	—	$1.75/330$	—	Ω/pF

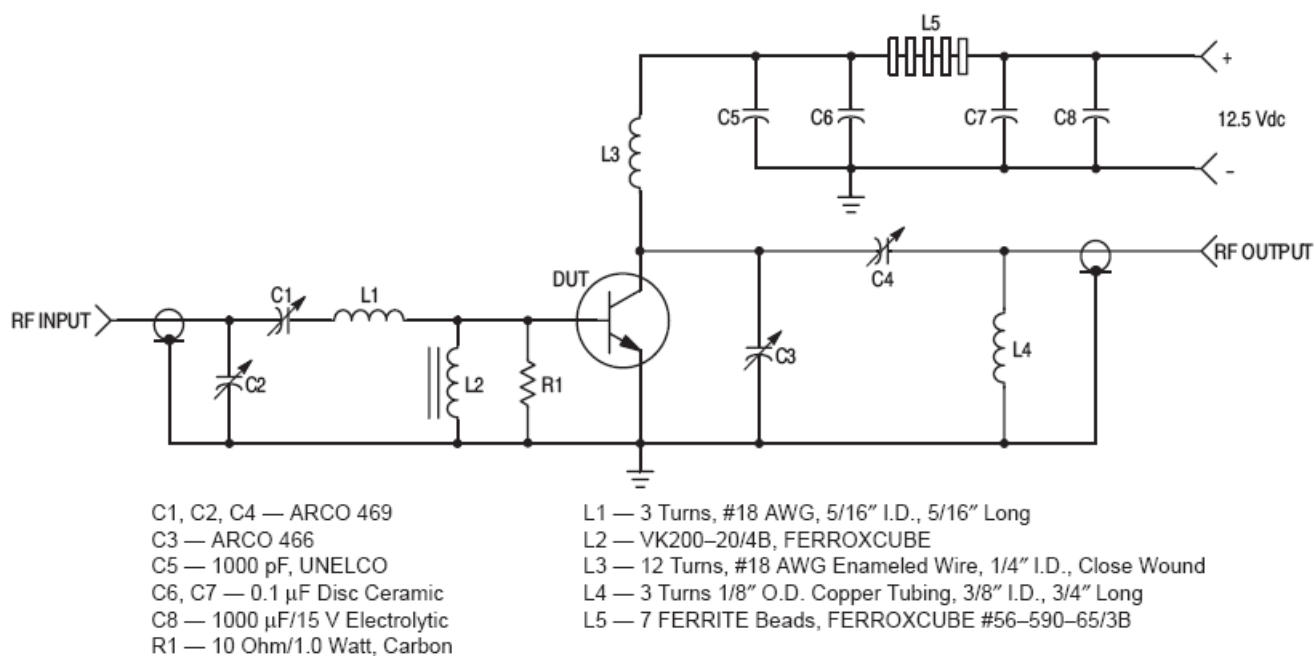


Figure 1. 30 MHz Test Circuit Schematic

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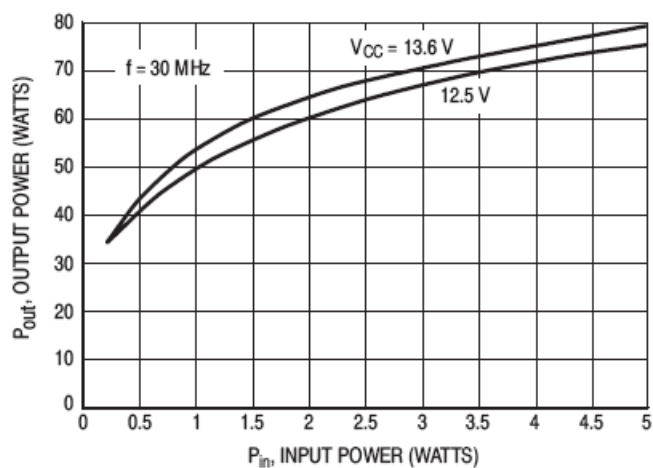


Figure 2. Output Power versus Input Power

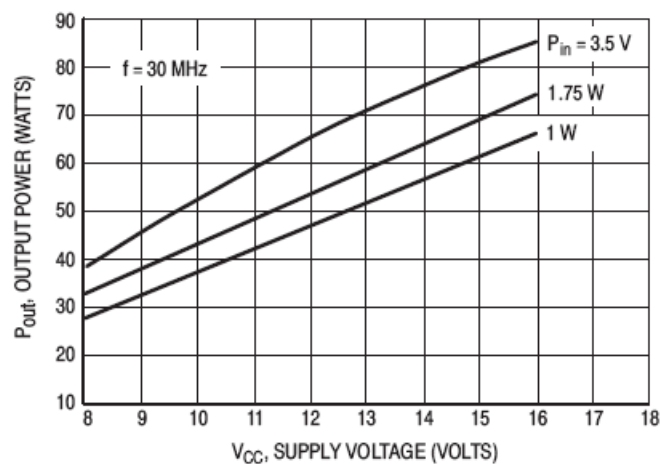
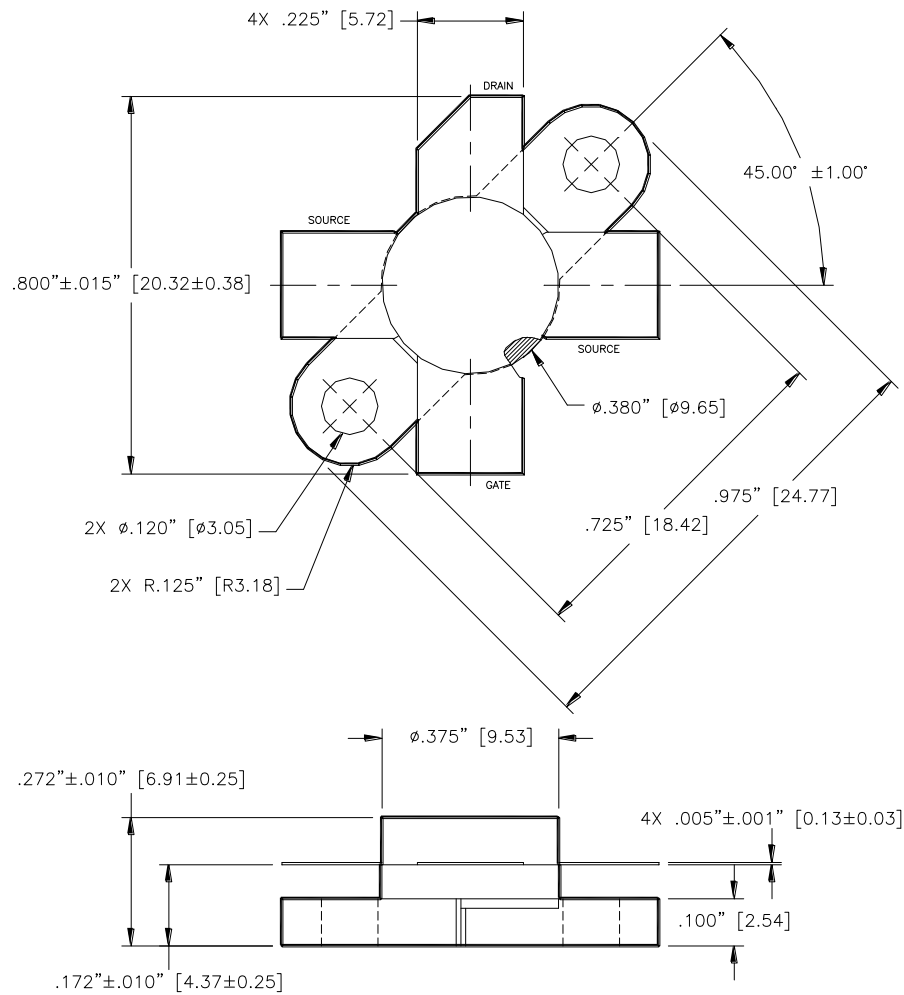


Figure 3. Output Power versus Supply Voltage

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Unless otherwise noted, tolerances are inches ±.005" [millimeters ±0.13mm]

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